

4. The electronic device of claim 3, wherein the laminate material comprises:
  - a core;
  - a dielectric material surrounding the core; and
  - a solder mask.
5. The electronic device of claim 4, wherein the laminate further includes a plated through hole.
6. (Amended) The electronic device of claim 4, further including a connection between at least one contact on the first surface and at least one contact on the second surface.
7. (Amended) The electronic device of claim 6, further including a ground shield over the connection.
8. The electronic device of claim 4, wherein the core comprises a material selected from the group consisting of: copper-invar-copper, copper, stainless steel, nickel, iron and molybdenum.
9. The electronic device of claim 4, wherein the dielectric material comprises polyimide.
10. The electronic device of claim 1, wherein the contacts comprise ball grid array connections.
11. The electronic device of claim 1, wherein the first substrate comprises a chip package.

12. The electronic device of claim 1, wherein the second substrate comprises a printed circuit board.

13. The electronic device of claim 1, further comprising a stiffener frame attached to the connector.

14. The electronic device of claim 13, wherein the stiffener frame is adhesively attached to the connector.

15. The electronic device of claim 13, wherein the stiffener frame surrounds a perimeter of the connector.

16. The electronic device of claim 13, wherein the stiffener frame is removably attached to the connector.

17. The electronic device of claim 13, wherein the stiffener frame is attached to a surface of the connector.

18. The electronic device of claim 13, wherein the stiffener frame comprises a material selected from the group consisting of: plastic, metal and ceramic.

19. The electronic device of claim 13, wherein the stiffener frame comprises a heat sink.

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20. (Twice Amended) A connector system, comprising:

a flexible substrate;

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at least three contacts on a first surface of the substrate; and

at least three contacts on a second surface of the substrate, wherein the contacts on the first surface of the substrate are alternatingly off-set from the contacts on the second surface of the substrate.

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21. The connector system of claim 20, wherein the flexible substrate comprises a laminate material.

22. The connector system of claim 21, wherein the laminate material comprises:

a core;

a dielectric material surrounding the core; and

a solder mask.

23. The connector system of claim 22, wherein the laminate material further includes a plated through hole.

24. (Amended) The connector system of claim 22, further including a connection between at least one contact on the first surface and at least one contact on the second surface.

25. (Amended) The connector system of claim 22, further including a ground shield over the connection.

26. The connector system of claim 22, wherein the core comprises a material selected from the group consisting of: copper-invar-copper, copper, stainless steel, nickel, iron and molybdenum.

27. The connector system of claim 22, wherein the dielectric material comprises polyimide.

28. The connector system of claim of 20, wherein the contacts comprise ball grid array connections.

29. The connector system of claim 20, further including a stiffener frame.

30. The connector system of claim 29, wherein the stiffener frame is removably attached to the flexible substrate.

31. (Twice Amended) A method of forming an electronic device, comprising:

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providing a flexible connector having a plurality of alternating contacts on a first surface and a second surface of the flexible connector, wherein at least three contacts in succession on the first surface alternate with at least three contacts in succession on the second surface; and

attaching the flexible connector between a first substrate and a second substrate via the contacts.

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32. The method of claim 31, wherein the flexible connector comprises a laminate material.

33. The method of claim 31, wherein the contacts comprises ball grid array connections.

34. The method of claim 31, wherein select contacts on the first surface of the flexible connector are off-set from select contacts on the second surface of the flexible connector.

35. The method of claim 31, wherein the first substrate comprises a chip package.

36. The method of claim 31, wherein the second substrate comprises a printed circuit board.

37. (Twice Amended) A method of forming an electronic device, comprising:

providing a first substrate;

providing a second substrate;

providing a flexible connector having at least three alternating contacts on a first surface and at least three alternating contacts on a second surface of the connector; and

attaching the contacts on the first surface of the connector to the first substrate and the contacts on the second surface of the connector to the second substrate.

38. The method of claim 37, wherein the first substrate comprises a chip package.

39. The method of claim 37, wherein the second substrate comprises a printed circuit board.

40. The method of claim 37, wherein the flexible connector comprises a laminate material.

41. The method of claim 37, wherein the contacts comprise ball grid array connections.

42. A method of forming an electronic device, comprising:

providing a flexible connector having a plurality of alternating contacts on a first surface and a second surface of the flexible connector, and a stiffener frame surrounding a perimeter edge of the flexible connector; and

attaching the flexible connector between a first substrate and a second substrate via the contacts.

43. (Amended) A connector system, comprising:

a flexible substrate; and

at least three contacts located at a far distance to a neutral point (DNP) on a first surface and at least three contacts located at a far distance to a neutral point (DNP) a second surface of the substrate, wherein the contacts are off-set.

#### REMARKS

Claims 1 and 3-43 are pending in this application. Claim 2 has been canceled. Claims 1 and 3-43 stand rejected. Reconsideration and allowance in view of the following remarks are respectfully requested.

Claims 1-4, 8, 10-12, 20-22, 26, 28 and 31-43 are rejected under 35 U.S.C. §102(b) as being anticipated by Appelt *et al.* (US 5,900,675, hereinafter "Appelt"). Claims 5-7 and 23-25 are rejected under 35 U.S.C. §103(a) as being unpatentable over Appelt in view of Nguyen (US 5,477,933). Claims 9 and 27 are rejected under 35 U.S.C. §103(a) as being unpatentable over Appelt in view of